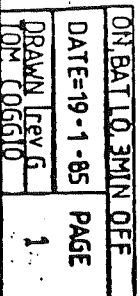


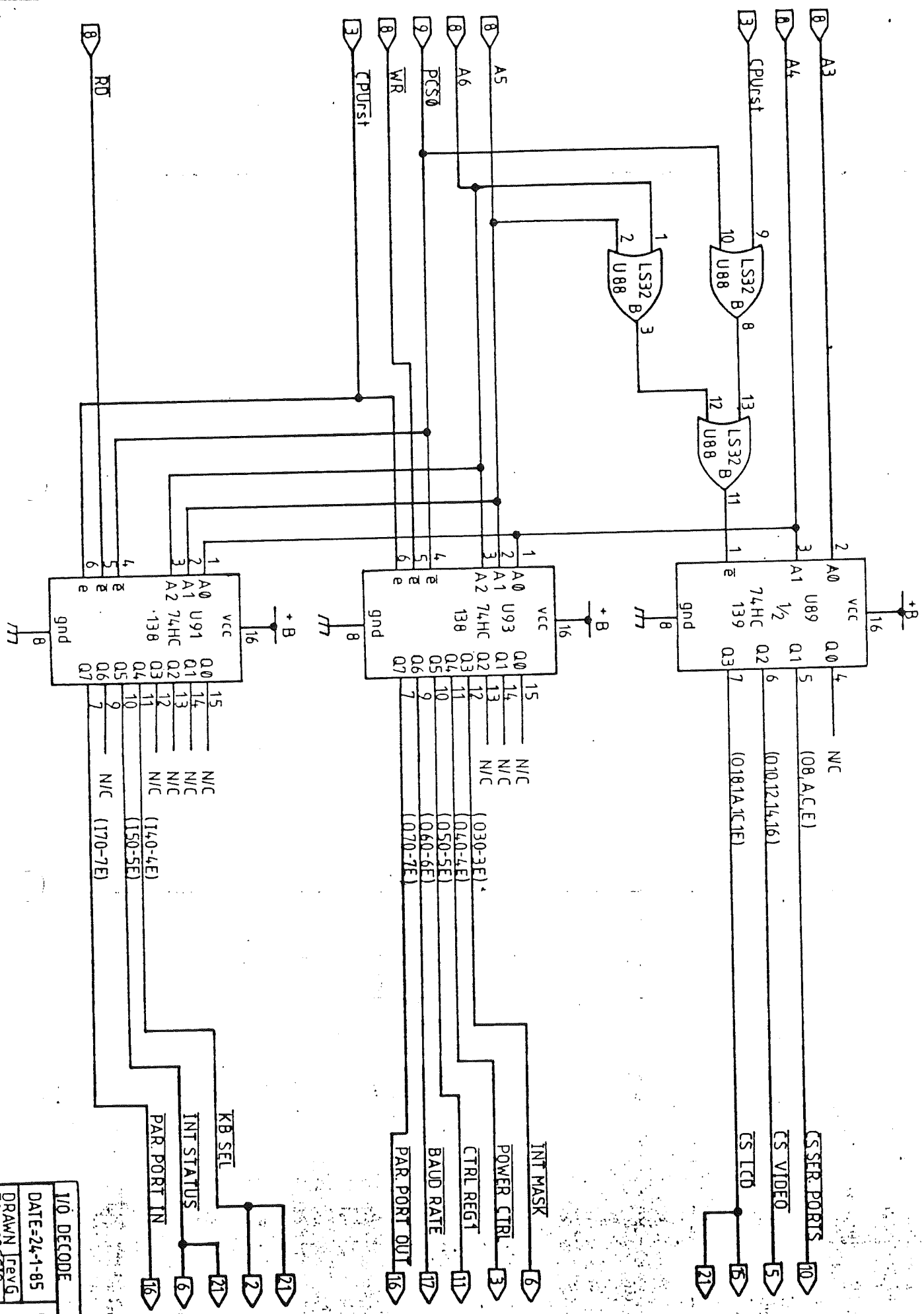
KOOKABURRA CIRCUIT DIAGRAMS

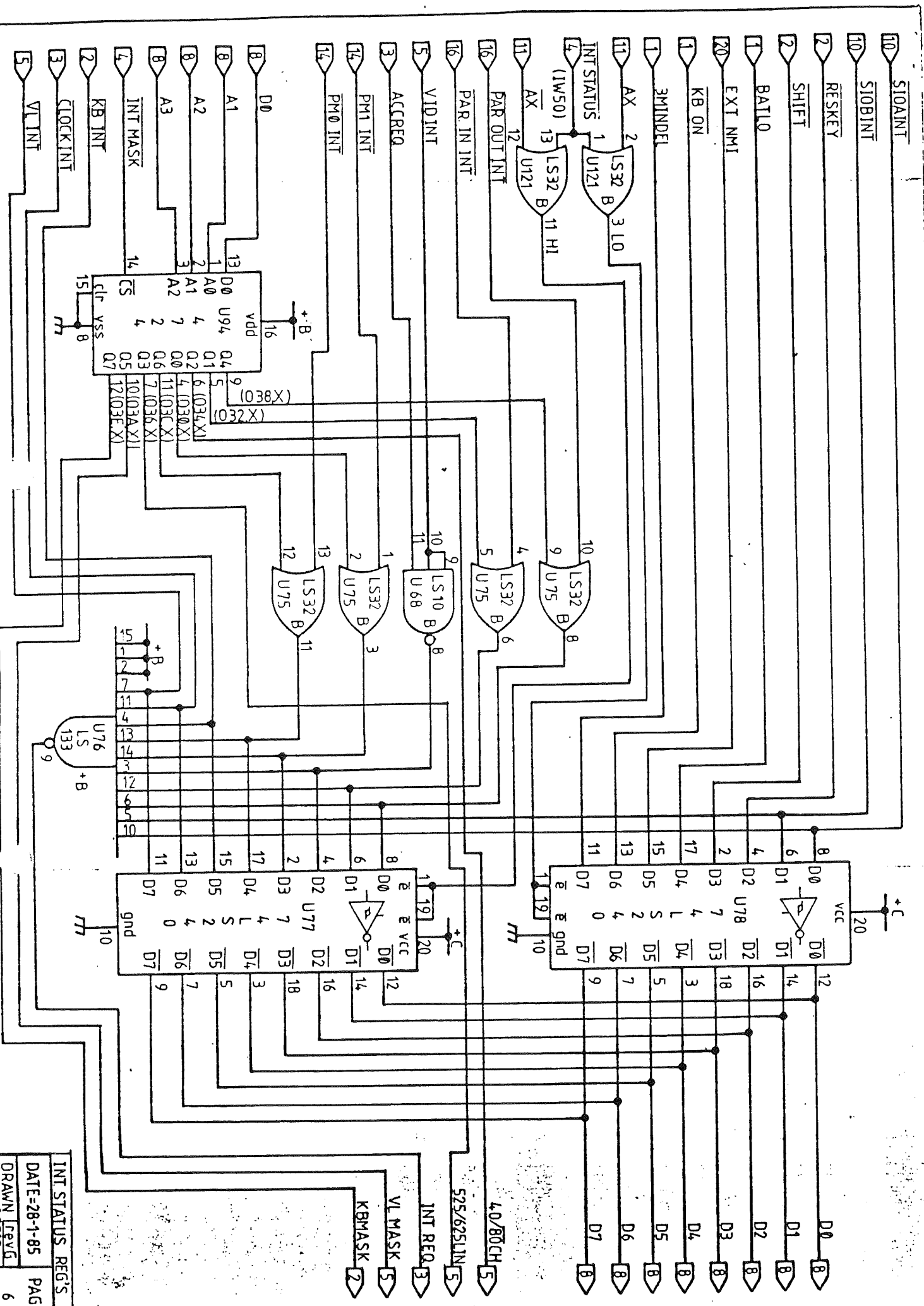
PAGE

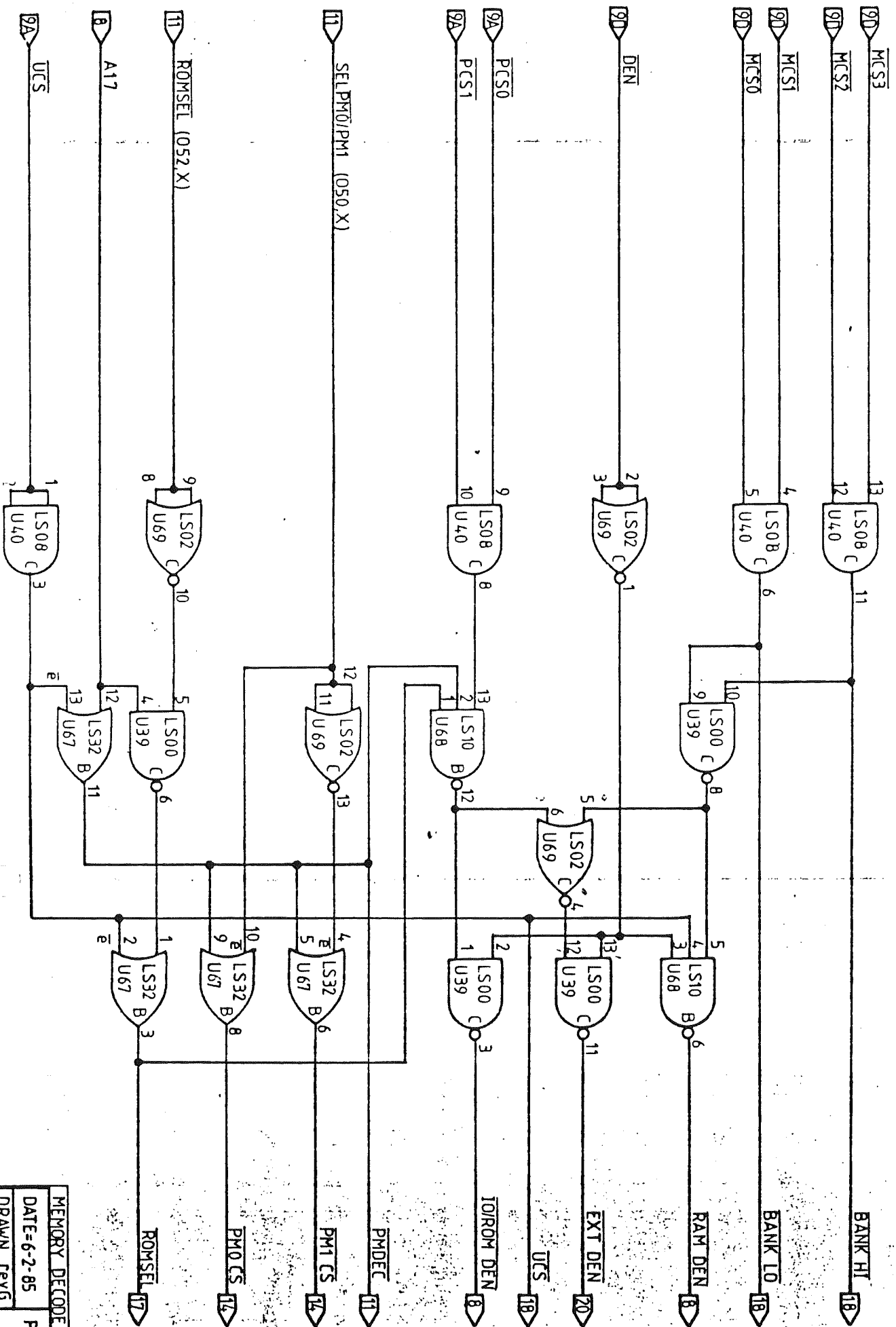
TITLE

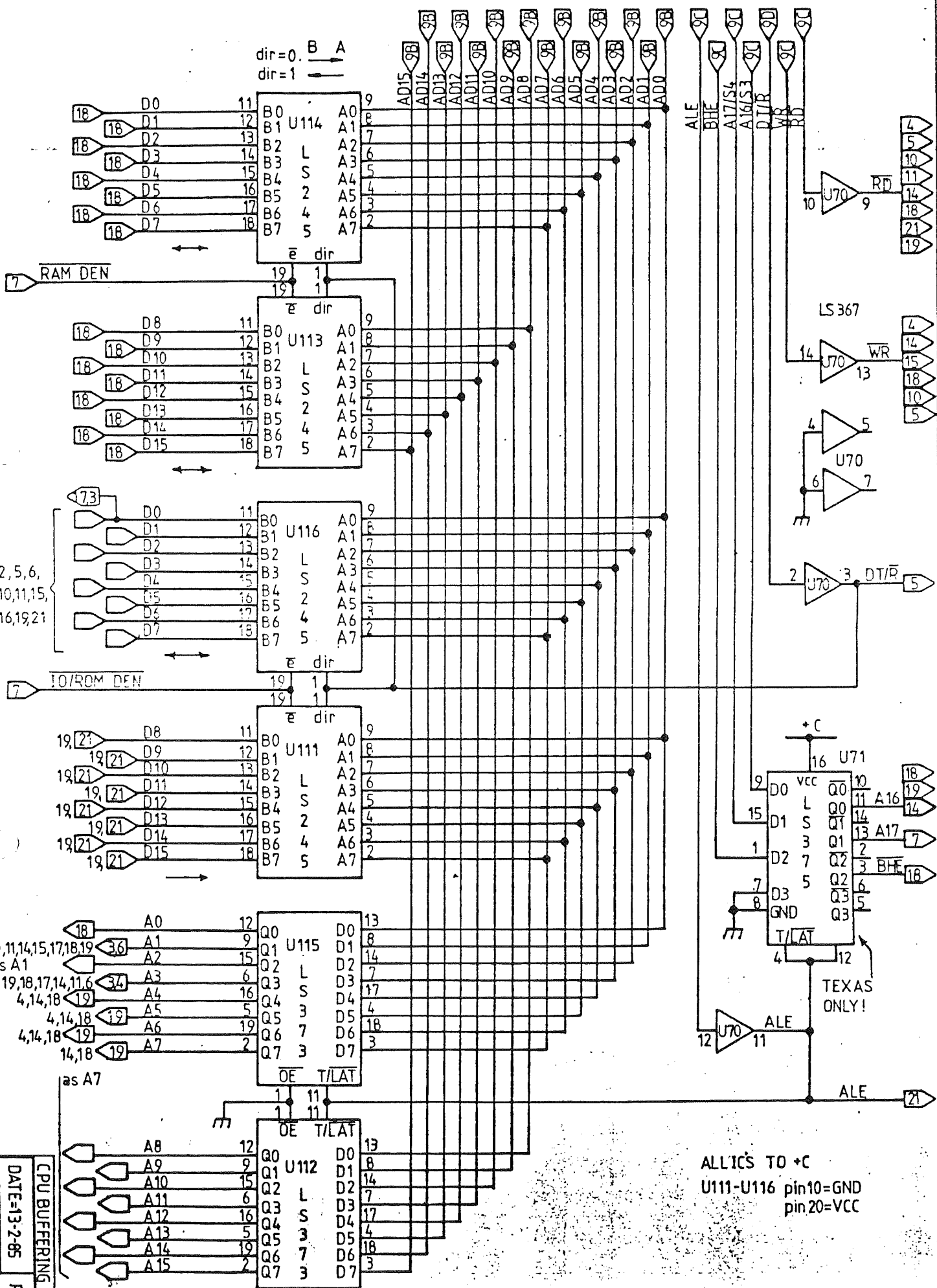
- | | |
|------|---|
| 1. | ON.BAT LO .3MIN OFF. |
| 2. | KEYBOARD. |
| 3. | POWER UP AND INTERRUPT. |
| 4. | I/O DECODE. |
| 5. | VIDEO. $\Rightarrow$ Missing |
| 6. | INT.STATUS REGISTERS. |
| 7. | MEMORY DECODE. |
| 8. | CPU BUFFERING. |
| 9. | CPU (PHISICAL TOP VIEW). |
| 10. | SERIAL PORTS. |
| 11. | CLOCK.ALARM.KEY CLICK AND.
CONTROL REGISTER. |
| 12. | KEYBOARD MATRIX. |
| 13. | POWER SUPPLY. |
| 14. | PERSONALITY MODULES. |
| 15A. | 8 LINE LCD DRIVER. |
| 15B. | 16 LINE LCD DRIVER. |
| 17. | BAUD RATE GENERATOR. |
| 18. | STATIC RAM. |
| 19. | EPROMS. |
| 20. | 60 WAY PORT. $\Rightarrow$ Missing |
| 21. | WAIT AND AX GENERATION. |



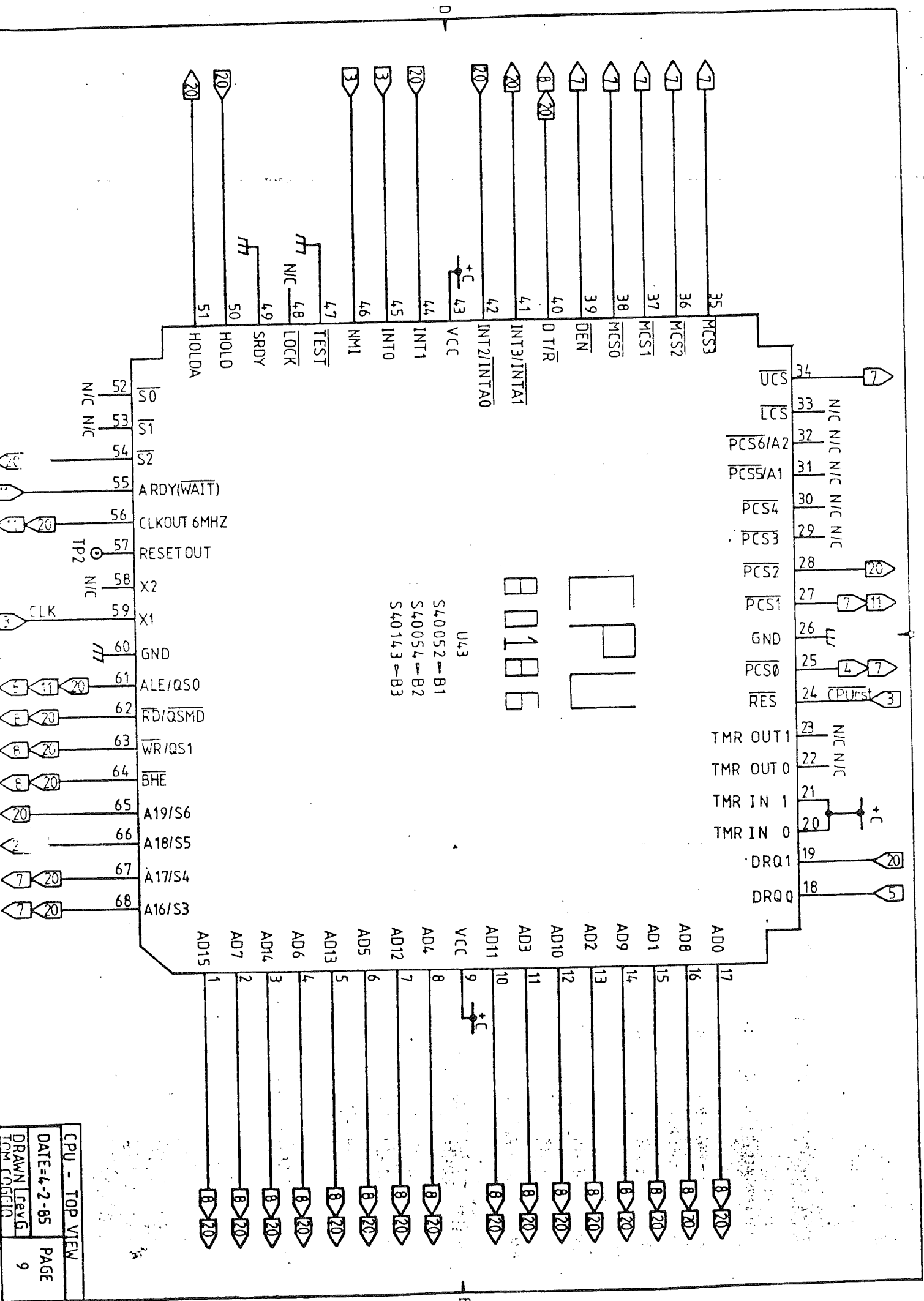


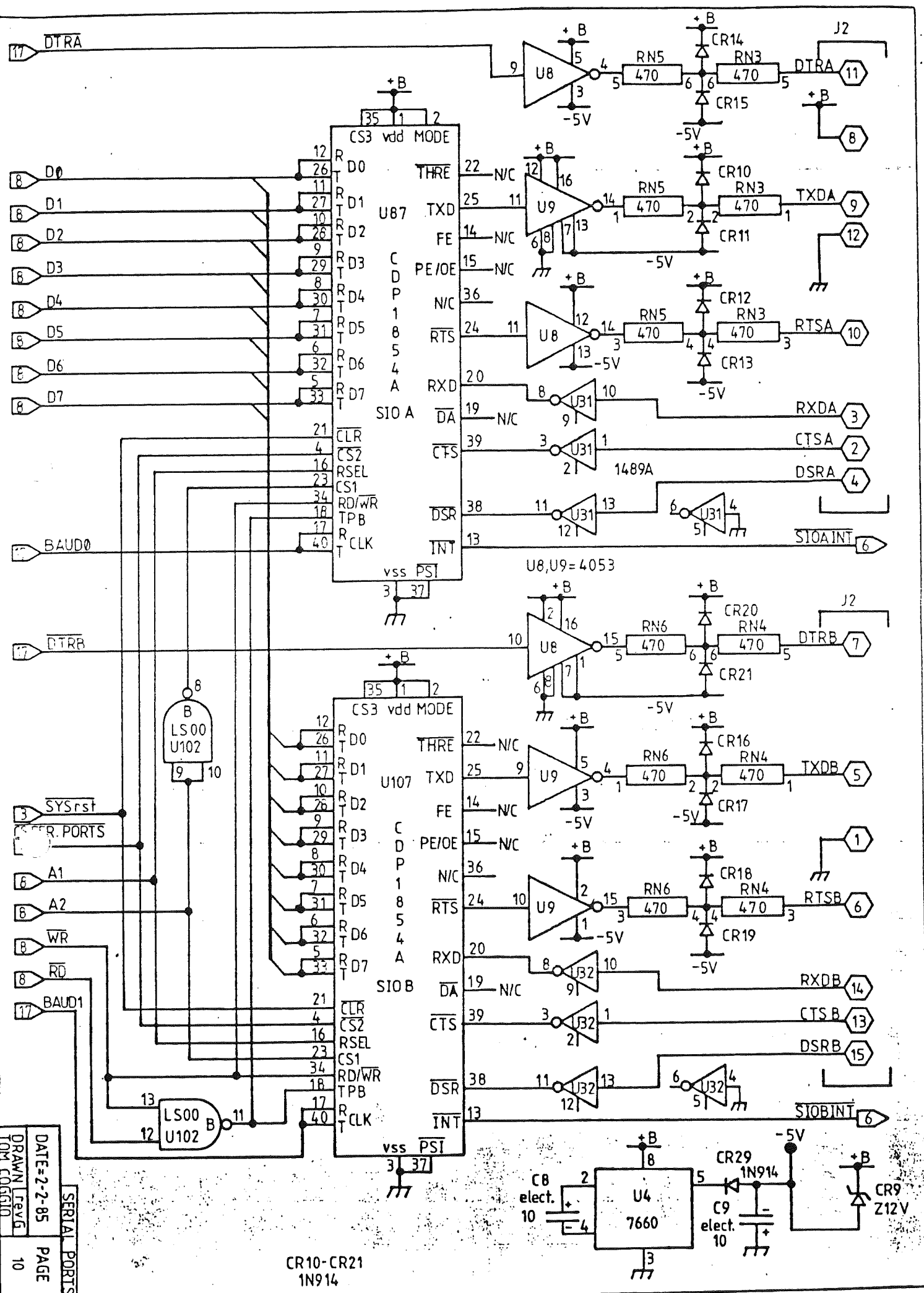




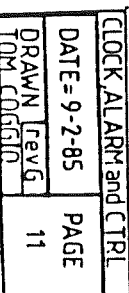


ALL IC'S TO +C
U111-U116 pin10=GND
pin20=VCC

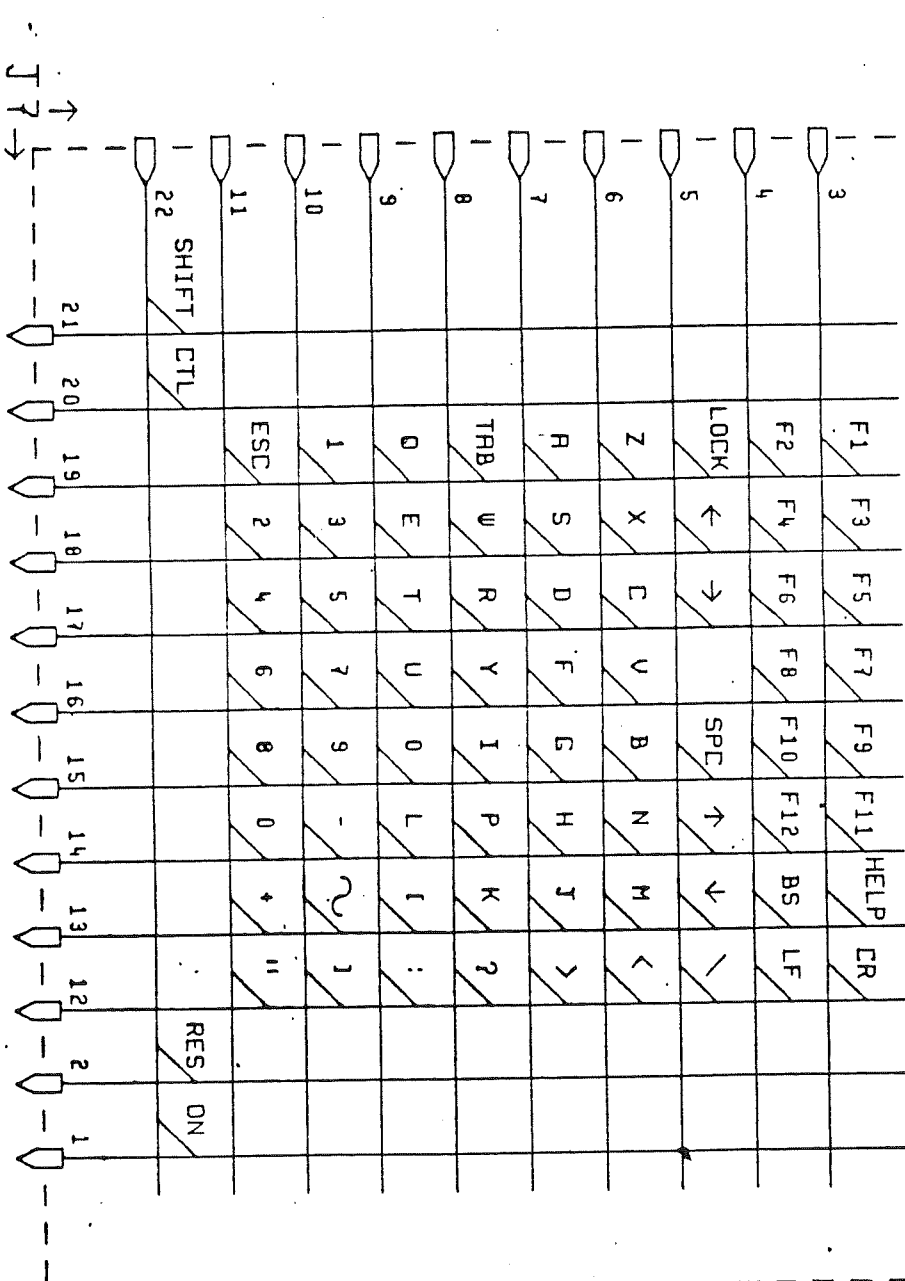




CR10-CR21
1N914



ON MAIN BOARD



DULMONT ELECTRONIC SYSTEMS

SYSTEM DYNAMICS

COOPER, TILDEN:

FUNCTION

DECEMBER 22, 1964

xx.

FILES

SYDNEY. AUSTRALIA.

Time

DATE

REF ID: A670484

[illegible]

140484
PT

DATE 12/1/80

今

[illegible]

DEC 1973

Time

SCIENTIFIC DIRECT

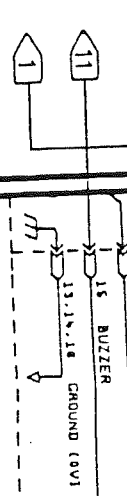
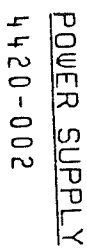
KEYBOARD

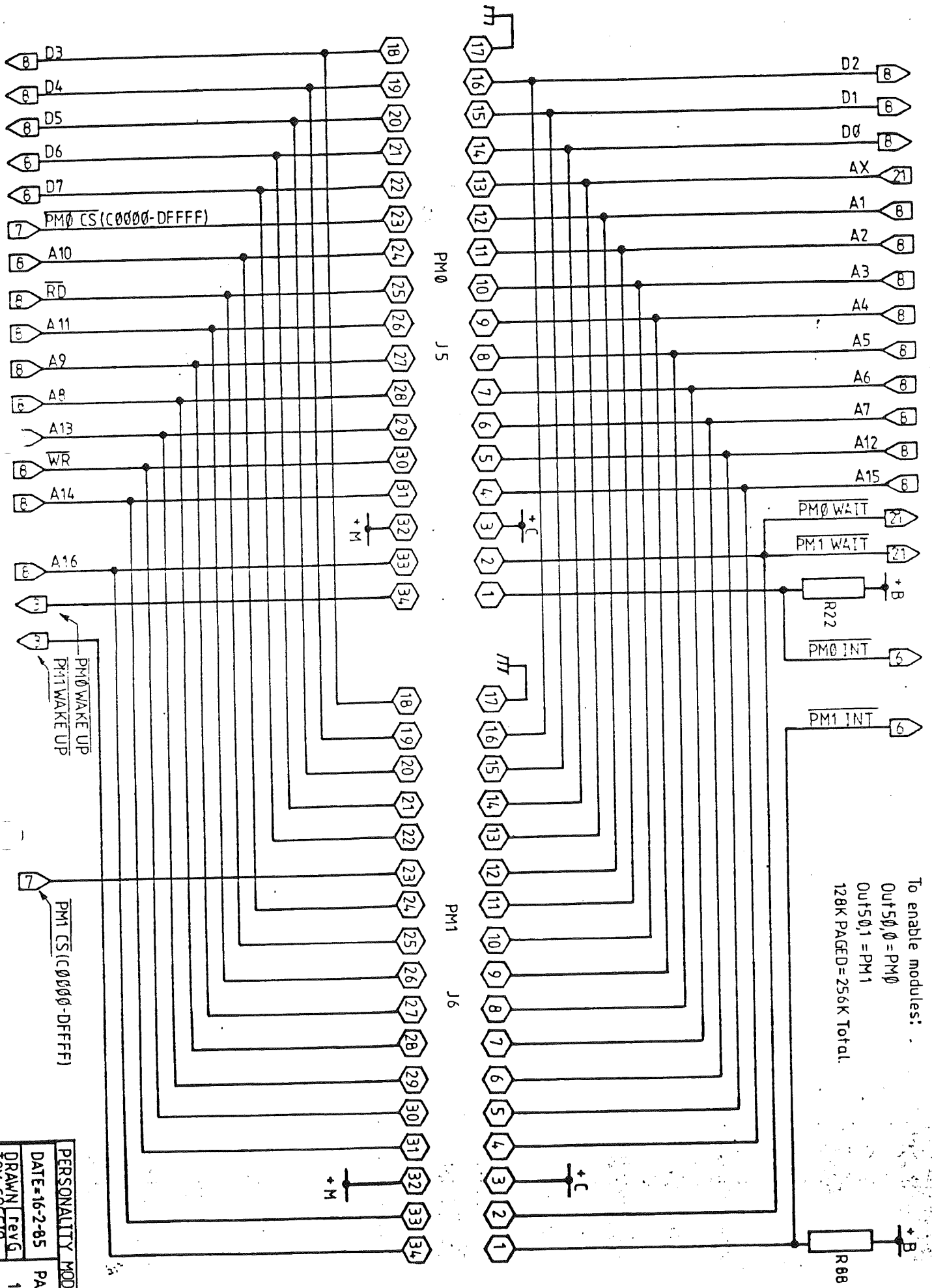
5

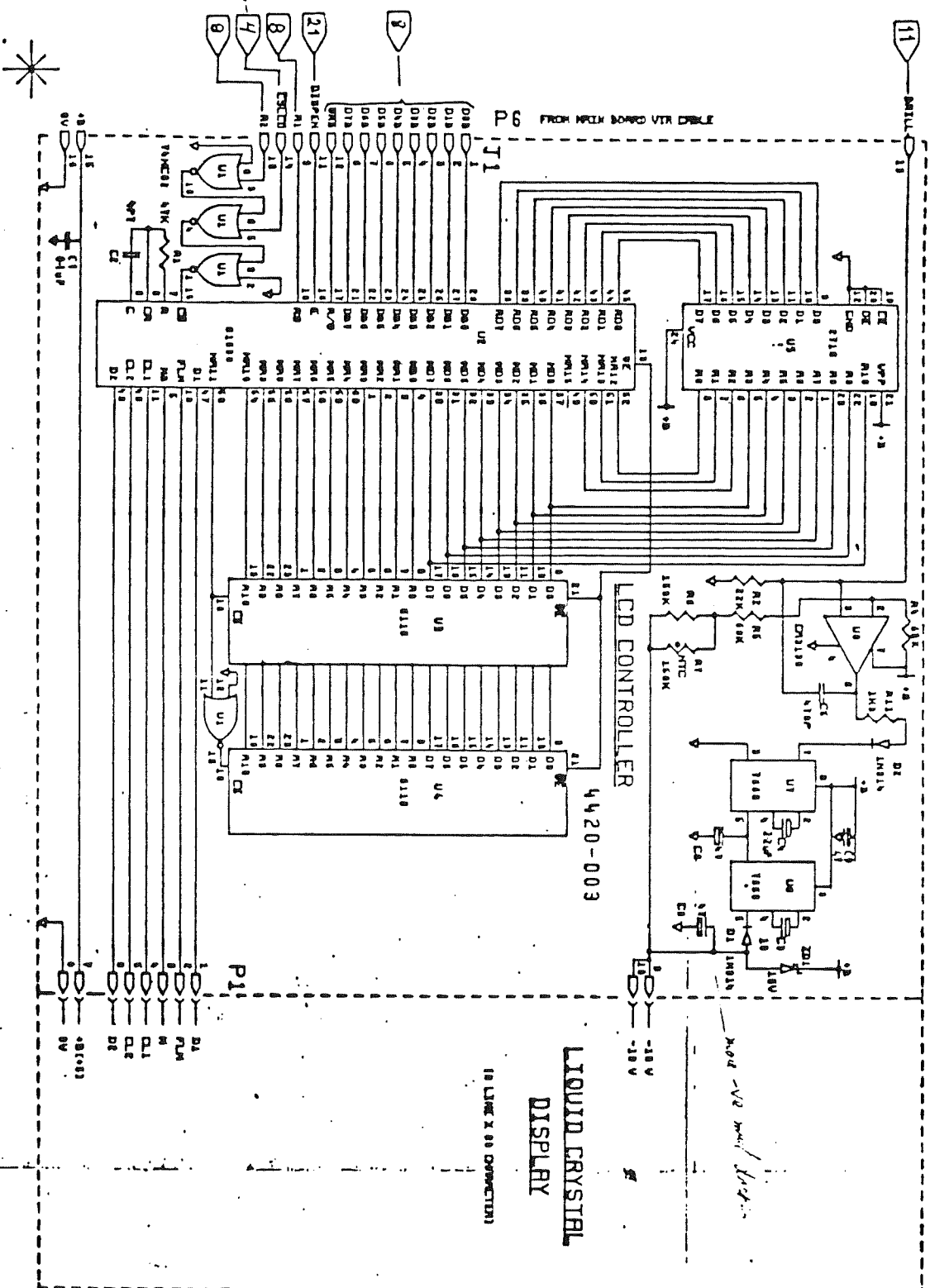
DRUGS

2

44-20-001

57





THIRD ANGLE PROJECTION

DULMONT ELECTRONIC SYSTEMS

SYDNEY, AUSTRALIA

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN MM

DATE 070486

DATE 210584

DATE 210584

DATE 210584

DATE 210584

CO-ORDINATE TOLERANCE: 0.1

PRECISION: 0.1

PRECISION: 0.1

PRECISION: 0.1

PRECISION: 0.1

PRECISION: 0.1

PRECISION: 0.1

PRECISION: 0.1

PRECISION: 0.1

PRECISION: 0.1

CO-ORDINATE TOLERANCE: 0.1

PRECISION: 0.1

PRECISION: 0.1

PRECISION: 0.1

PRECISION: 0.1

PRECISION: 0.1

PRECISION: 0.1

PRECISION: 0.1

PRECISION: 0.1

PRECISION: 0.1

CO-ORDINATE TOLERANCE: 0.1

PRECISION: 0.1

PRECISION: 0.1

PRECISION: 0.1

PRECISION: 0.1

PRECISION: 0.1

PRECISION: 0.1

PRECISION: 0.1

PRECISION: 0.1

PRECISION: 0.1

CO-ORDINATE TOLERANCE: 0.1

PRECISION: 0.1

PRECISION: 0.1

PRECISION: 0.1

PRECISION: 0.1

PRECISION: 0.1

PRECISION: 0.1

PRECISION: 0.1

PRECISION: 0.1

PRECISION: 0.1

CO-ORDINATE TOLERANCE: 0.1

PRECISION: 0.1

PRECISION: 0.1

PRECISION: 0.1

PRECISION: 0.1

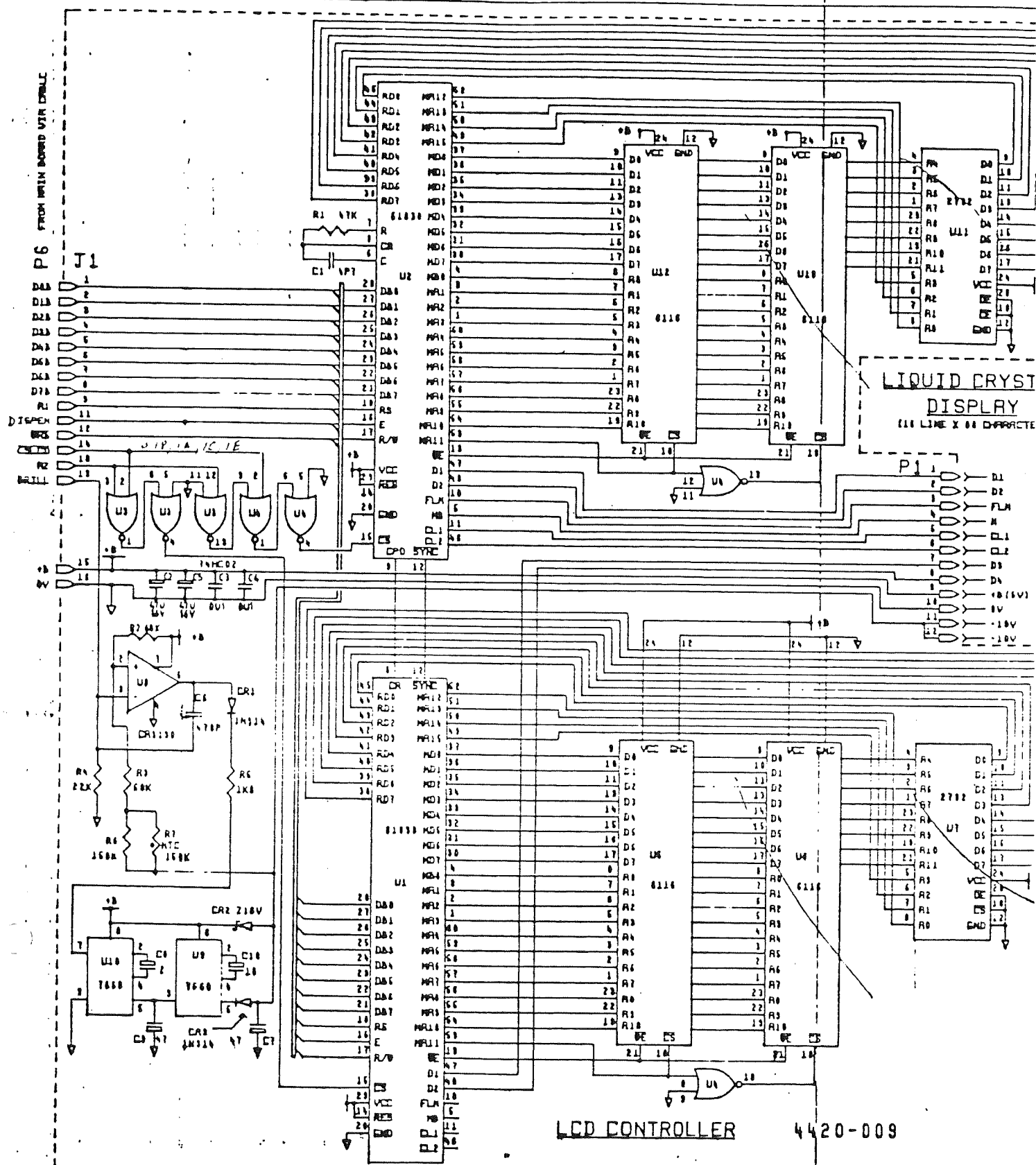
PRECISION: 0.1

PRECISION: 0.1

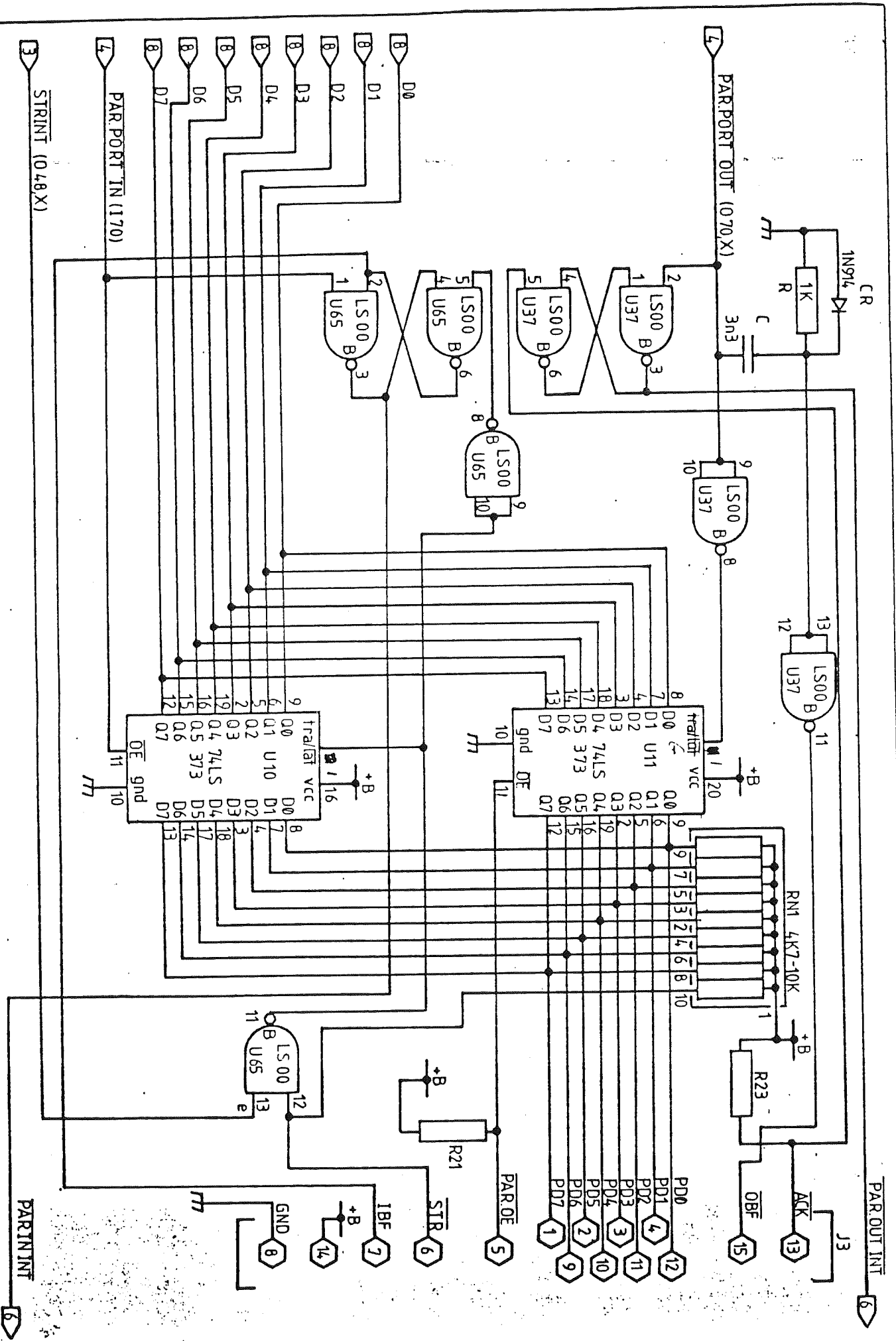
PRECISION: 0.1

PRECISION: 0.1

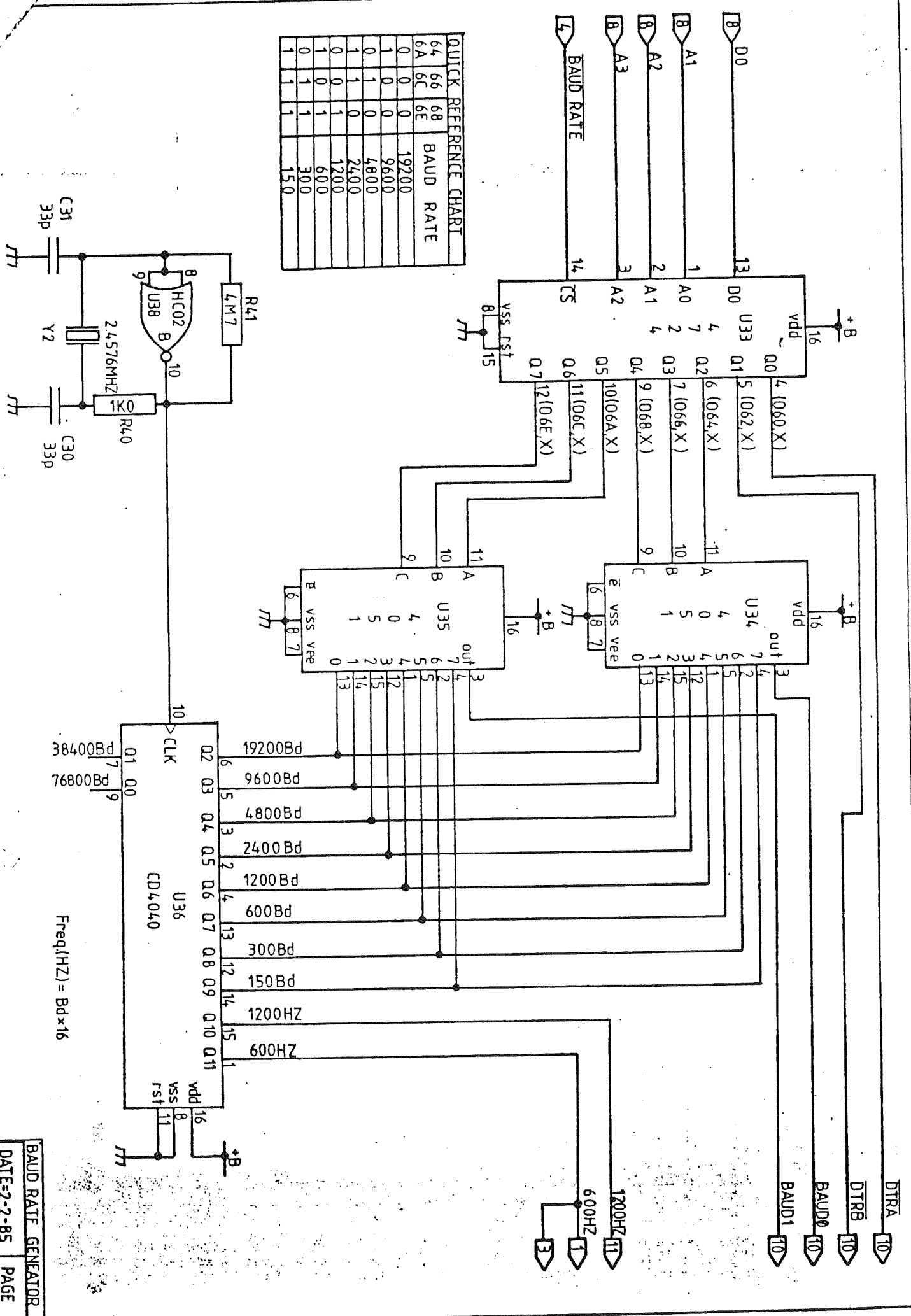
PRECISION: 0.1



PROJECTION		DULMONT ELECTRONIC SYSTEMS		
DATE	DATE	SYDNEY, AUSTRALIA,		
RFT	070484	TITLE SCHEMATIC DIAGRAM 16 LINE X 80 CHARACTER LCD CONTROLLER 15B		
CHK'D BY	DATE			
RFT	140484			
CHECKED	DATE			
C.L	210584			
ENG APP	DATE	SCALE	DRAWING NO.	TABLE
C.T	210584	NTS	4420-009	C



QUICK REFERENCE CHART	BAUD RATE
64 6A 6C 6E	19200
1 0 0 0	9600
0 1 0 0	4800
1 1 0 0	2400
0 0 0 1	1200
1 0 0 1	600
0 1 0 1	300
1 1 0 1	150



Freq(HZ) = Bd × 16

